



X-1068 US  
10/075,178

PATENT

IN THE UNITED STATES PATENT OFFICE

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OCT 23 2002

TECHNOLOGY CENTER 2800

Applicant: Andy H. Gan

Assignee: Xilinx, Inc.

Title: Method and Apparatus for Routing Interconnects to  
Devices with Dissimilar Pitches

Serial No.: 10/075,178

File Date: 2/14/2002

Examiner: Unknown

Art Unit: 2123

Docket No.: X-1068 US

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COMMISSIONER FOR PATENTS  
Washington, D. C. 20231

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Technology Center 2100

PRELIMINARY AMENDMENT

Dear Sir:

Before taking action in the above-referenced case, please  
make the following amendments to the claims.

IN THE CLAIMS

**Please add Claims 21-28 as follows.**

21. (New) A system, comprising:

an external memory circuit having one or more configuration  
data output terminals; and

a programmable logic device (PLD), the PLD comprising:

a programmable logic portion having a first vertical  
pitch and a first horizontal pitch;

an internal configuration memory having one or more  
configuration data input terminals coupled to the  
configuration data output terminals of the external memory  
circuit, and further having output terminals coupled to the  
programmable logic portion of the PLD;

an embedded core having a second vertical pitch and a  
second horizontal pitch, wherein the first vertical pitch  
and the second vertical pitch are different; and

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01 FC:1201  
02 FC:1202

84.00 CH  
144.00 CH